

Notice of References Cited	Application/Control No. 10/664,458	Applicant(s)/Patent Under Reexamination YAMAZAKI ET AL.	
	Examiner Christopher M. Raabe	Art Unit 2879	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2001/0002703	06-2001	Koyama, Jun	257/40
	B	US-2002/0070385	06-2002	Yamagata, Hirokazu	257/93
	C	US-5,793,457	08-1998	Tamai et al.	349/110
	D	US-2002/0074936	06-2002	Yamazaki et al.	313/504
	E	US-5,968,675	10-1999	Tamano et al.	428/690
	F	US-2001/0004469	06-2001	HIMESHIMA et al.	427/66
	G	US-6,069,443	05-2000	Jones et al.	313/504
	H	US-5,320,878	06-1994	Maya, Leon	427/573
	I	US-3,531,856	10-1970	BELL HAROLD B; et. al.	29/827
	J	US-5,866,919	02-1999	Kwon et al.	257/59
	K	US-6,359,320	03-2002	Yamazaki et al.	257/408
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 09235546 A	09-1997	Japan	HIMESHIMA et al.	C09K 11/06
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Producing Monolithic Light Emitting Diode Display Chips, IBM Tecnical Disclosure Bulletin, 16(1), June 1, 1997, pg 6
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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